

ARMmite F4 Std LCD Connections
(J1/FSMC connector – 32 pins)

1	Vss (Gnd) & 30,32
2	LCD RESET
3	FSMC_D15
4	FSMC_D14
5	FSMC_D13
6	FSMC_D12
7	FSMC_D11
8	FSMC_D10
9	FSMC_D9
10	FSMC_D8
11	FSMC_D7
12	FSMC_D6
13	FSMC_D5
14	FSMC_D4
15	FSMC_D3
16	FSMC_D2
17	FSMC_D1
18	FSMC_D0
19	FSMC_NOE
20	FSMC_NWE
21	FSMC_A18
22	FSMC_NE1
23	T_SCK
24	T_CS
25	T_MOSI
26	T_MISO
27	PENIRQ
28	LCD_BL (NC)
29	NC
30	Vss (Gnd) (NC)
31	Vdd 3v3 (NC)
32	Vss (Gnd)

East Rising 9" LCD Connections
(JP2/CON1 – (40 pins)

1	Vss (Gnd)
7	RESET
24	DB15
23	DB14
22	DB13
21	DB12
20	DB11
19	DB10
18	DB9
17	DB8
16	DB7
15	DB6
14	DB5
13	DB4
12	DB3
11	DB2
10	DB1
9	DB0
5	RD
6	RW
4	DC
3	CS
34	T_CLK
33	T_CS
35	T_DIN
36	T_DOUT
38	TPPEN
39	Vdd 5V - See Note #1
40	Vss (Gnd)
2	Vdd 5V - See Note #2

Note #1: If the East Rising 9" LCD panel is configured for SSD1963 PWM backlight control (via J3 / J4 jumper on LCD Panel), then Pin 39 on the LCD JP2 connector will be ignored and can be left disconnected.

If the J3 / J4 jumpers on the LCD Panel are configured for External Backlight control, connecting Pin 39 to Vdd **should** cause the backlight to be on at 100% brightness.

The configuration of the J3 / J4 jumpers seems to vary depending upon the version of the LCD PCB and/or the version of the ER-TFTM090-1 LCD Module datasheet being consulted. Please check your documentation.

Note #2: Vdd (5v) is **NOT** present on the F4 FSMC connector and must be sourced separately for connection to Pin 2 (& 39 if used) on the LCD JP2 connector.